

ECE 137 A Mid-Term Exam

Thursday February 6, 2014

Do not open exam until instructed to.

Closed book: Crib sheet and 1 page personal notes permitted

There are 2 problems on this exam, and you have 75 minutes.

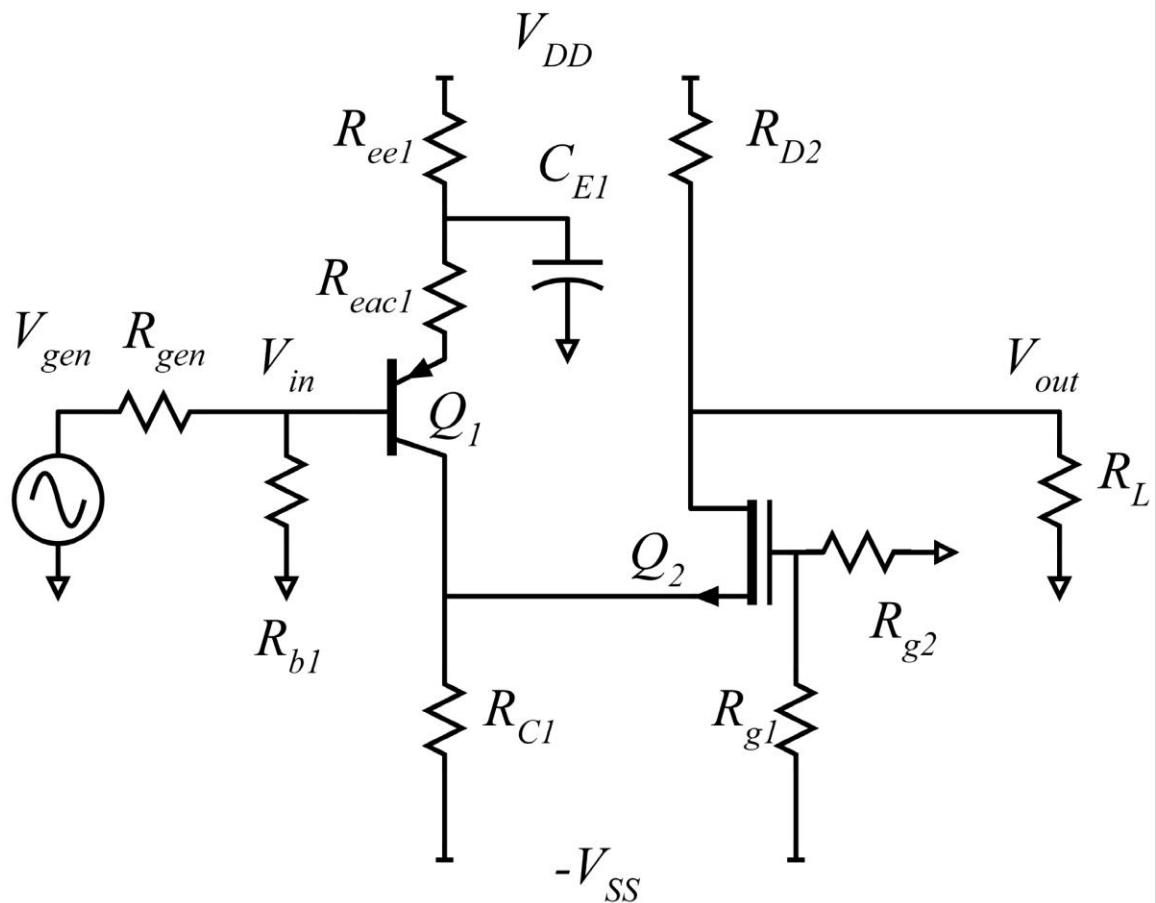
Use any and all reasonable approximations (5% accuracy is fine.) , ***AFTER STATING and approximately Justifying them.***

Name: _____

Part	Points Received	Points Possible
1a		7
1b		7
1c		6
1d		15
1e		15
1f		6
1g		14
2a		12
2b		13
2c		5
TOTAL		100

Problem 1, 70 points

You will be working on the circuit below:



Q1: $\beta = 50$, $V_A = 50$ V

Q2: Velocity-limited $V_{th} = 0.2$ V, $1/\lambda = \text{infinity}$, $\Delta V = L_g v_{th} / \mu = 0.1$ V, $c_{ox} v_{th} W_g = 5$ mA/V

The supplies are +3V and -3V

$R_{gen} = 1000$ Ohms, $R_L = 5,000$ Ohms. $R_{g2} = 50$ kOhms, $R_{eac1} = 37$ Ohms, $R_{b1} = 10$ kOhms
 C_{E1} is very large (AC short-circuit)

Part a, 7 points

DC bias.

V_{in} is at (approximately) zero volts DC.

The gate of Q2 is to be biased at -2 Volts

The drain is to be biased at zero volts.

Q1 is to be biased at 2 mA emitter current

Q2 is to be biased at 1 mA drain current.

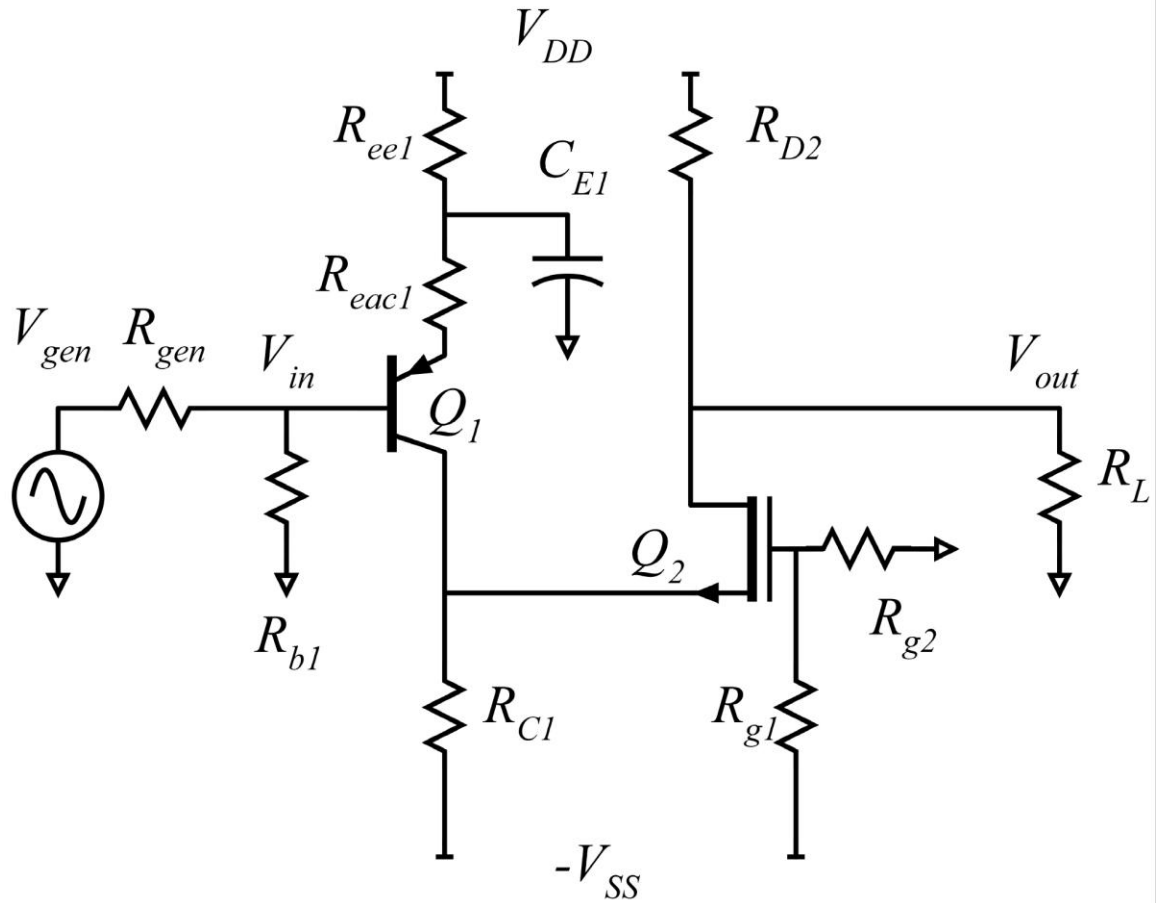
Find the following:

$R_{c1} = \underline{\hspace{2cm}}$ $R_{g1} = \underline{\hspace{2cm}}$ $R_{ee1} = \underline{\hspace{2cm}}$

$R_{g1} = \underline{\hspace{2cm}}$

Part b, 7 points

DC bias



On the circuit diagram above, label the DC voltages at **ALL nodes** and the DC currents through **ALL resistors**

Part c, 6 points

Find the small signal parameters of Q1 and Q2.

Transistor Q1: $g_m =$ _____ $R_{ce} =$ _____ $R_{be} =$ _____

Transistor Q2: $g_m =$ _____ $R_{ds} =$ _____

Part d, 15 points.

Find the small signal voltage gain (V_{d2}/V_{s2}) of Q2 and Q2's small-signal input resistance.

$V_{d2}/V_{s2} =$ _____

$R_{in,q2} =$ _____

Part e, 15 points

Find the small signal voltage gain (V_{c1}/V_{b1}) of Q1 and the *** amplifier *** input resistance.

$V_{c1}/V_{b1} =$ _____

$R_{in, amplifier} =$ _____

Part f, 6 points

Find (V_{out}/V_{in}) , (V_{in}/V_{gen}) and (V_{out}/V_{gen})

$$(V_{out}/V_{in}) = \underline{\hspace{10em}}$$

$$(V_{in}/V_{gen}) = \underline{\hspace{10em}}$$

$$(V_{out}/V_{gen}) = \underline{\hspace{10em}}$$

Part g, 14 points

Now you must find the maximum signal swings. Find the output voltage due to saturation and cutoff in Q2. **Give the sign (+ or -) in your answers below.**

Cutoff of Q1; Maximum ΔV_{out} resulting = _____

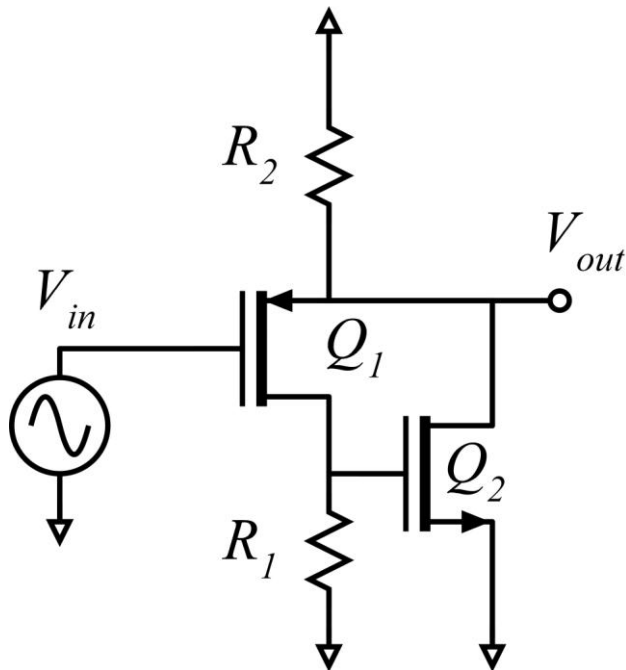
Saturation of Q1; Maximum ΔV_{out} resulting = _____

Cutoff of Q2; Maximum ΔV_{out} resulting = _____

Knee voltage of Q2; Maximum ΔV_{out} resulting = _____

Problem 2, 30 points

nodal analysis



You will be working on the circuit to the left.

Ignore DC bias analysis. You don't need it.

Transistor 1 has transconductance g_{m1} .

Transistor 2 has transconductance g_{m2} .

The drain-source resistances R_{ds} of both transistors are infinity (so you don't need to draw it!)

Part a, 12 points

Draw the small-signal equivalent circuit

Part b, 13 points

Find, by nodal analysis, a small-signal expression for V_{out}/V_{in} .

$V_{out}/V_{in} =$ _____

Part c, 5 points

$g_{m1} = 1 \text{ mS}$ $g_{m2} = 10 \text{ mS}$, $R_1 = 1\text{k}\Omega$, $R_2 = 2\text{k}\Omega$
Give a numerical value for V_{out}/V_{in} .

$V_{out}/V_{in} = \underline{\hspace{2cm}}$