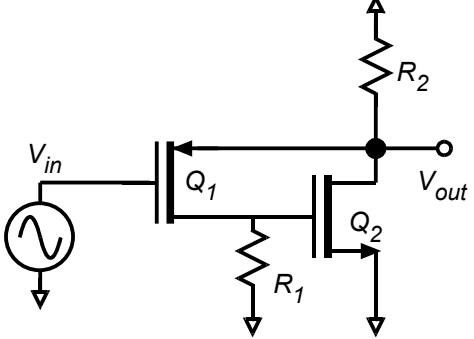


ECE137a Problem set 3

	1). Common-emitter amplifier with emitter degeneration. Q1: $\beta=100$, $V_A = \text{infinity V}$. The supplies are +5V and -5 V. You will bias the transistor with 10 mA collector current. The DC collector bias voltage is 0V. R_L is 500Ω, R_{gen} is 100Ω, R_b is $1\text{ k}\Omega$, R_{EAC} is 7.4Ω, C_{EE} is very large. Assume that it is an AC short-circuit.
a) Give all resistor values b) Find the small signal V_{out}/V_{in}, V_{in}/V_{gen} and V_{out}/V_{gen}	c) Find the amplifier input and output impedances d) Find the maximum positive-going and negative going outputs.
	2) The MOSFET has $v_{th} = 0.25\text{ Volt}$, $K_\mu = 0.55\text{mA/V}^2 \cdot (W_g / 1\mu\text{m})$, $K_v = 0.69\text{mA/V} \cdot (W_g / 1\mu\text{m})$, $\Delta V = 0.625\text{V}$ and $1/\lambda = 10\text{ Volts}$. The supplies are +2V and -2 V. You are to bias the transistor at 0.5 mA drain current, with 0.5V DC drain voltage, and with -0.5 V DC source voltage. $R_{SAC} = 10\Omega$, $R_G = 1\text{ M}\Omega$, $R_{gen} = 75\text{ k}\Omega$, $R_L = 10\text{ k}\Omega$
a) Give all resistor values	b) Find the small signal V_{out}/V_{in}, V_{in}/V_{gen} and V_{out}/V_{gen}
a	3) Common-drain stage. The transistor has $K_\mu = \mu c_{gs} W_g / 2L_g = 0.55\text{mA/V}^2 \cdot (W_g / 1\mu\text{m})$, $K_v = c_{gs} v_{inj} W_g = 0.69\text{mA/V} \cdot (W_g / 1\mu\text{m})$, $\Delta V = v_{inj} L_g / \mu = 0.625\text{V}$, $V_{th} = 0.3\text{V}$, $1/\lambda = 10\text{V}$. The supplies are +1V and -1 V. You are to bias the transistor at 5mA drain current, and with -0.7 V DC source voltage. $R_g = 1\text{ M}\Omega$,

	$R_{gen}=100\text{ k}\Omega$, $R_L=1000\text{ }\Omega$. C_{out} is very large (AC short-circuit at the signal frequency)
a) find the gate width W_g and R_{ss} . b) Find the small signal voltage gain V_{out}/V_{in} and the amplifier small-signal input resistance.	c) find the maximum positive and negative going output signal swings before clipping
	4) . Nodal Analysis exercise. This is a "super-buffer". Ignore DC bias analysis. You don't need it. The two transistors have transconductance g_{m1} and g_{m2} respectively. Their drain-source resistances R_{ds1} and R_{ds2} are both infinity. a) Compute V_{out}/V_{in} by nodal analysis. b) find numerical values of V_{out}/V_{in} given $g_{m1}=10\text{ mS}$, $g_{m2}=100\text{ mS}$, $R_1=10\text{ k}\Omega$, $R_2=1\text{ k}\Omega$.