

## **InP/In<sub>0.53</sub>Ga<sub>0.47</sub>As/InP Double Heterojunction Bipolar Transistors on a GaAs Substrate Using InP Metamorphic Buffer Layer**

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Double heterojunction bipolar transistors (DHBTs) have applications [1-3] in high frequency communications and radar. Though InGaAs/InP HBTs grown on InP substrates show the best high-frequency performance, InP substrates have several disadvantages compared to GaAs substrates. InP substrates are expensive and 100-mm-diameter InP substrates are extremely fragile and are readily broken during processing. Thus, several groups have recently pursued metamorphic growth of InP-based DHBTs on GaAs substrates [4].

Here we report InP-based DHBTs grown on GaAs using InP as the metamorphic buffer layer. While AlGaAsSb and InAlAs have been explored as the metamorphic buffer layers, InP metamorphic layers grown in our laboratory have substantially better measured thermal conductivity – 16.1 W/mK for InP, 10.5 W/mK for InAlAs, and 8.4 W/mK for AlGaAsSb metamorphic layers. With a typical HBT geometry and  $2 \times 10^5$  W/cm<sup>2</sup> dissipation, a thermal analysis indicates that use of the InP buffer layer will reduce the operating junction temperature 16-39 °C, a difference which can have substantial impact upon device reliability.

InP/In<sub>0.53</sub>Ga<sub>0.47</sub>As/InP DHBTs were grown on GaAs substrate using a Varian Gen II MBE system. The 1.5 μm thick InP metamorphic buffer layer was grown at 480°C directly on the GaAs substrate. During buffer layer growth, the reflection high energy electron diffraction (RHEED) pattern showed strong streaks, indicating two-dimensional growth, though the RHEED intensity was slightly smaller than observed with lattice-matched growth.

Triple-mesa HBTs were fabricated using optical projection lithography and selective wet etching. The emitter-base junction is 0.3 mm by 8 mm, while the base-collector junction is 1.1 mm x 11 mm. Fig. 1 shows the common emitter characteristics; β = 27 is obtained. Current-gain and power-gain cut-off frequencies  $f_t = 165$  GHz and  $f_{max} = 92$  GHz were measured at  $I_C = 7.0$  mA and  $V_{CE} = 1.5$  V, as determined by a -20 dB/decade extrapolation from DC-45 GHz measurements (figure 2). These cutoff frequencies are over a 2:1 higher than prior reported results for metamorphic HBTs.

### References

- [1] Asbeck, P., Chang, F., Wang, K.-C., Sullivan, G., Cheung, D. 'GaAs-based Heterojunction Bipolar Transistors for Very High Performance Electronic Circuits', IEEE Proceedings, 1993, 81, (12), pp. 1709-1726
- [2] Matsuoka, Y., Yamahata, S., Kurishima, K., Ito, H., 'Ultrahigh-speed InP/InGaAs Double-Heterostructure Bipolar Transistors and Analysis of Their Operation', Japanese Journal of Applied Physics, 1996, 35, pp.5646-5654
- [3] Oka, T., Hirata, K., K. Ouchi, H. Uchiyama, K. Mochizuki, T. Nakamura, 'Small-scale InGaP/GaAs HBTs with WSi/Ti base electrode and buried SiO<sub>2</sub>', IEEE Transactions on Electron Devices, 1998, 45, (11), pp.2276-82
- [4] Zheng, H.Q., Radhakrishnan, K., Wang, H., Yuan, K.H., Yoon, S.F., Ng, G.I.: 'Metamorphic InP/InGaAs double-heterojunction bipolar transistors on GaAs grown by molecular-beam epitaxy', Appl. Phys. Lett., 2000, 77, (6), pp.869-871

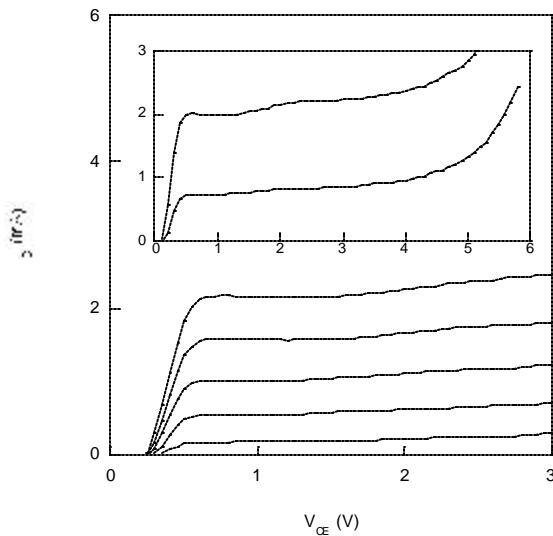


Fig. 1: Common-Emitter DC characteristics of a 0.3 mm x 8 mm emitter device. Base current steps are 20 mA. The inset shows the 5 V BVCEO breakdown voltage.

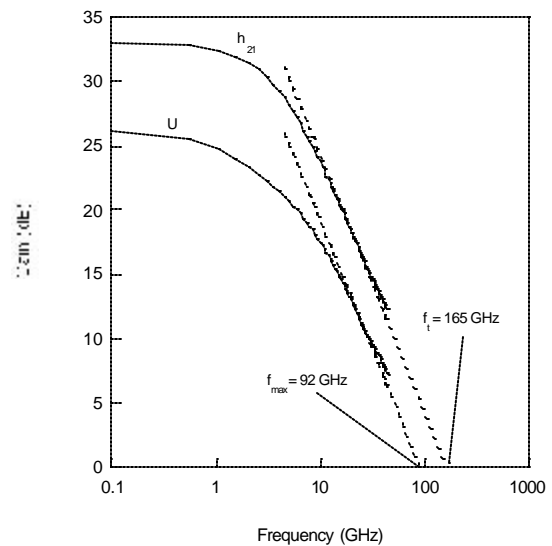


Fig. 2: Small signal current gain and unilateral power gain vs. frequency at  $I_C=7.0$  mA and  $V_{CE}=1.5$  V.