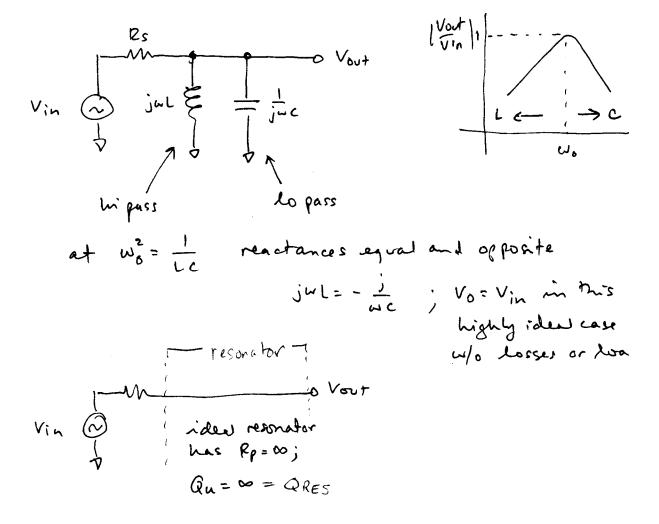
Resonant Circuits

Widely used when bandpass characteristics are required for interstage matching or filtering.



If Rp, the Favaillel equivalent of component loss is finite, then Vour < Vin.

Quality factor, Q

Reactive components such as capacitors and inductors are often described with a figure of merit called Q. While it can be defined in many ways, it's most fundamental description is:

$$Q = \omega \frac{\text{energy stored}}{\text{average power dissipated}}$$

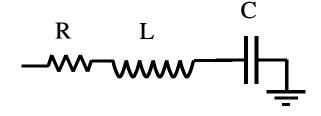
Thus, it is a measure of the ratio of stored vs. lost energy per unit time. Note that this definition does not specify what type of system is required. Thus, it is quite general. Recall that an ideal reactive component (capacitor or inductor) stores energy

$$E = \frac{1}{2} C V_{pk}^2$$
 or $\frac{1}{2} L I_{pk}^2$

Since any real component also has loss due to the resistive component, the average power dissipated is

$$P_{avg} = \frac{1}{2} I_{pk}^2 R = \frac{V_{pk}^2}{2R}$$

If we consider an example of a series resonant circuit.



At resonance, the reactances cancel out leaving just a peak voltage, Vpk, across the loss resistance, R. Thus, Ipk = Vpk/R is the maximum current which passes through all elements. Then,

$$Q = \omega_O \frac{LI_{pk}^2/2}{I_{pk}^2 R/2} = \frac{\omega_O L}{R} = \frac{1}{\omega_O RC}$$

In terms of the series equivalent network for a capacitor shown above, its Q is given by:

$$Q = \frac{1}{\omega RC} = \frac{X}{R}$$

where we pretend that the capacitor is resonated with an ideal inductor at frequency ω . X is the capacitive reactance, and R is the series resistance. Since this Q refers only to the capacitor itself, in isolation from the rest of the circuit, it is called unloaded Q or Q_U. The higher the unloaded Q, the lower the loss. Notice that the Q decreases with frequency.

The unloaded Q of an inductor is given by

$$Q_U = \frac{\omega_o L}{R}$$

where R is a series resistance as described above. Note that Q is proportional to frequency for an inductor. The Q of an inductor will depend upon the wire diameter, core material (air, powdered iron, ferrite) and whether or not it is in a shielded metal can.

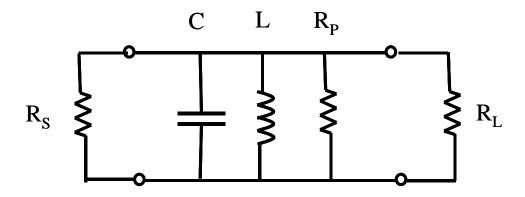
It is easy to show that for a parallel resonant circuit, the Q is given by susceptance/conductance:

$$Q = \frac{B}{G}$$

where B is the susceptance of the capacitor or inductor and G is the shunt conductance.

Loaded Q.

When a resonant circuit is connected to the outside world, its total losses (let's call them R_P or G_P) are combined with the source and load resistances, R_S and R_L . For example,



Here is a parallel resonant circuit (C,L and R_P)connected to the outside. The total Q of this circuit is called the loaded Q or Q_L and is given by

$$Q_L = \omega_o C(R_P \| R_S \| R_L)$$

$$Q_L = \frac{\omega_o C}{G_S + G_L + G_P} = \frac{\omega_o C}{G_{total}} = \frac{suscep \tan ce}{conduc \tan ce}$$

The significance of this is that Q_L can be used to predict the bandwidth of a resonant circuit. We can see that higher Q_L leads to narrower bandwidth.

$$BW = \frac{\omega_o}{Q_L}$$

where

$$\omega_o = \frac{1}{\sqrt{LC}}$$

1. So, large C will increase the loaded Q at a given resonant frequency and reduce bandwidth.

- 2. Or, we could vary G_{total}. How?
- A. Unloaded Q (Q_U) is an attribute of the passive L and C components and affects G_P . This will vary if we change L and C. And, $G_P = G_C + G_{ind}$,

Capacitor:

$$Q_U(capacitor) = \frac{\omega_o C}{G_C}$$

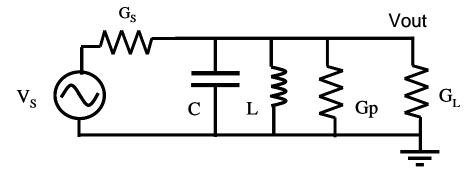
Inductor:

$$Q_U(inductor) = \frac{1}{\omega_o L G_{ind}}$$

Using this to set the bandwidth of a resonator is not a good idea. If Q_U becomes comparable to Q_L , then the loss in the resonator becomes very high as we shall see.

Insertion Loss

The resonator can be used as a bandpass filter or matching network. In these applications, insertion loss can be important.



Consider this resonant LCR circuit. When w = wo,

$$\frac{V_{out}(j\omega_o)}{V_S} = \frac{G_S}{G_S + G_L + G_P} < 1$$

Typically, $G_S = G_L = G$. We need to find S_{21} to determine insertion loss. Recall that $|S_{21}|^2 =$ transducer gain = Power delivered to the load/available power from the source when the source and load are both Zo.

First note that

$$\frac{Q_L}{Q_U} = \frac{G_P}{G_P + 2G}$$

And,

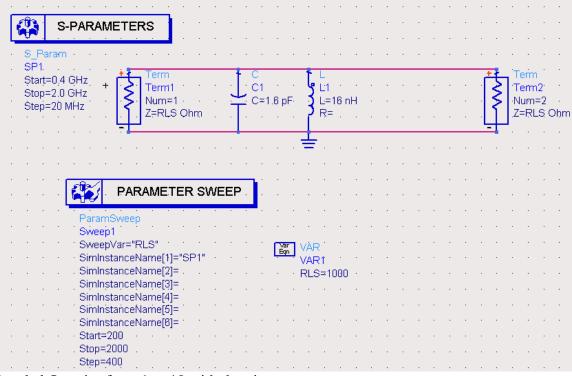
$$S_{21} = \frac{2V_{out}}{V_S} = 1 - \frac{Q_L}{Q_U} = \frac{2G}{G_P + 2G}$$

So, insertion loss (dB):

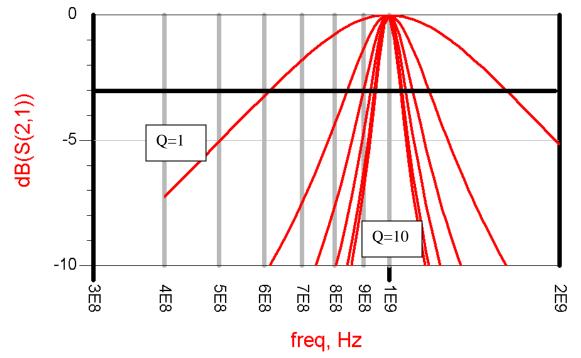
$$IL = 20\log\left(1 - \frac{Q_L}{Q_U}\right)$$

Thus, narrow bandwidth where Q_L and Q_U are similar can be very lossy.

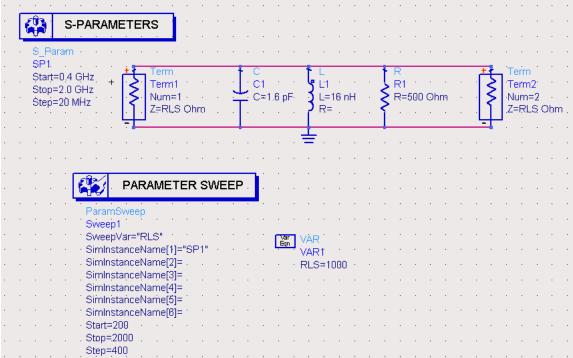
Here is an example of a simple resonant circuit. The unloaded Q is infinite, since no losses are included in the network. We see that there is no insertion loss in this case.



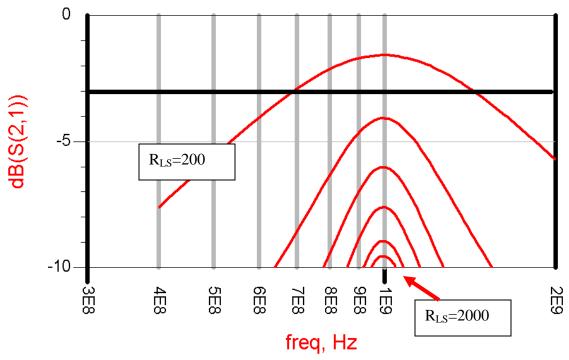
Loaded Q varies from 1 to 10 with the given parameter sweep.



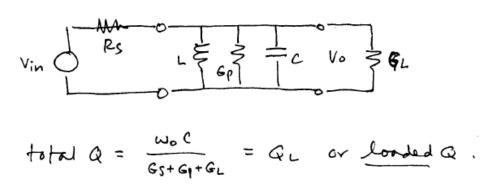
Now, the circuit is modified to include a 500 ohm resistor (R_P) in parallel with the LC network. This resistance represents the parallel equivalent loss due to both the L and the C. So, now we have a finite unloaded Q.



Note that the insertion loss increases as loaded Q, Q_L , approaches Q_U . Sweeping RLS, we see at resonance, the reactances cancel, and we are left with a resistive divider. Vout = Vin $[R_1/(2R_1+R_{LS})]$.



 $S21 (dB) = 20 \log(2Vout/Vin)$



$$BW = \frac{\omega_o}{Q_L}$$

But, we must make sure that $Q_U >> Q_L$ to avoid excessive losses.

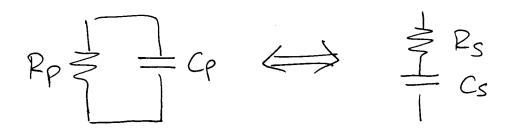
So, if really narrow bandwidth is required, the solution generally requires multiple resonators or more complicated bandpass filter approaches or mechanical structures like quartz crystal filters.

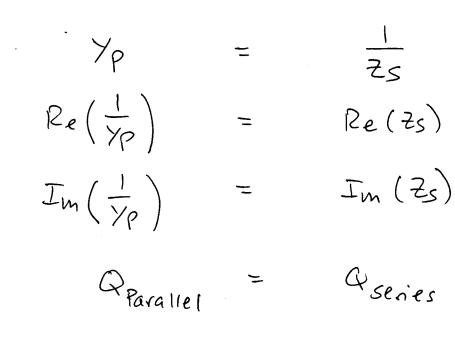
3. How can you set G_S and G_L ? Aren't these dictated by the generator and load?

Not necessarily! We can use tapped C matching circuits to transform source and load impedances to whatever we desire (assuming we don't increase loss too much by approaching Q_U).

But, first, we will introduce a convenient approximation which can be used to transform a parallel equivalent circuit into a series equivalent circuit and vice-versa.

Useful approximation for series parallel conversion (at a single frequency)





$$\frac{R_P}{X_P} = \varphi = \frac{X_S}{R_S}$$

$$Re(Z_S) = R_S = \frac{1/p_p}{1/p_p^2 + \omega^2 C_p^2} = Re\left(\frac{1}{\gamma_p}\right)$$
$$= \frac{Vp_p}{1/p_p^2 + Q^2 R_p^2} = \frac{R_p}{1+Q^2}$$

This leads to:

$$R_{s} = R_{p} \left(\frac{1}{Q^{2}+1} \right)$$
$$X_{s} = X_{p} \left(\frac{Q^{2}}{Q^{2}+1} \right)$$

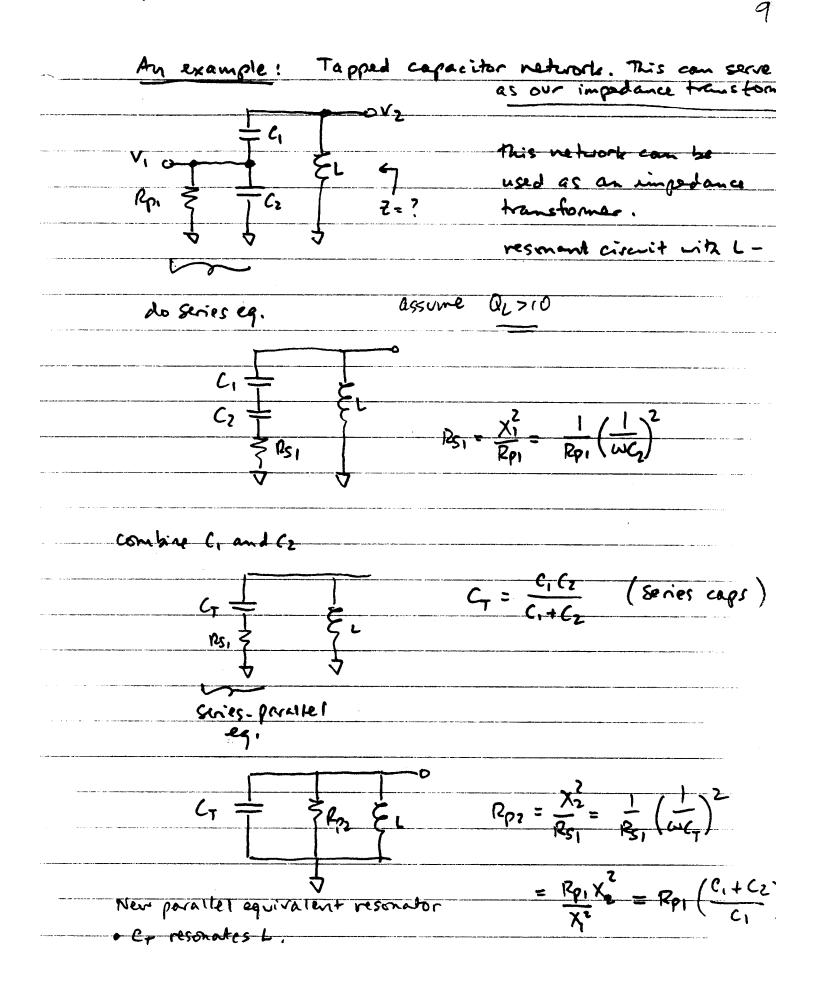
if Q>10:

$$X_{S} \stackrel{\sim}{=} X_{P} = X$$

$$P_{P} \stackrel{\simeq}{=} Q^{2} P_{S}$$

$$P_{P} R_{S} = X^{2}$$

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Or, if we define
$$N = \frac{C_1 + C_2}{C_1}$$

we get $R_{p2} = R_{p_1} n^2$

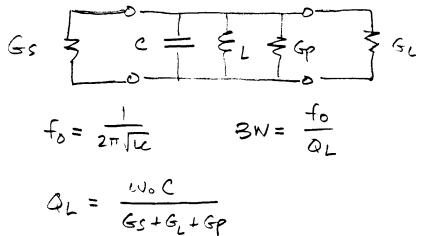
1. n is like the turns ratio on a transformer 2. but much easier to build than a transformer

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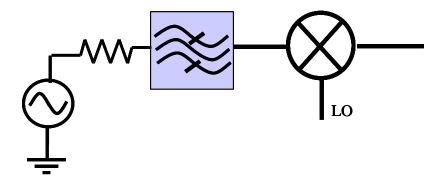
me same that you could transform impedances using a tapped capacitor network.

Let's see how we can use this network in conjunct witz resonators to build bandpass filters with particular fo and ZW.



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Example. Design bandpass matching network to transform 50 ohms to the input of an SA602 mixer.

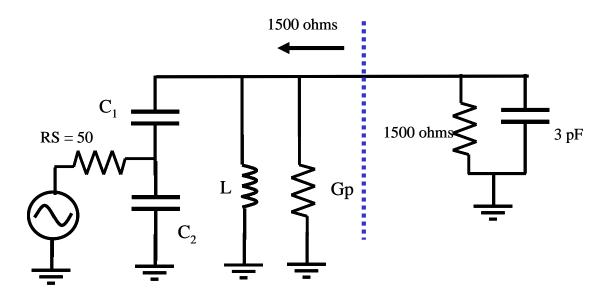


The mixer input impedance is given on the data sheet as

 $Z_{IN} = 1.5k \parallel 3 \text{ pF}$ for the SA602 IC.

Choose a convenient frequency for this exercise: $159 \text{ MHz} = 1 \times 10^9 \text{ rad/sec.}$

Use the tapped C procedure to transform 50 ohms to 1500 ohms. Include the 3 pF into the equivalent series capacitance of C1 and C2. The equivalent parallel resistance, R_P , due to the unloaded Q of the components should also be taken into account in calculating the loaded Q and the transformation ratio needed to match the source and load.



We need to specify a bandwidth to begin the process. Let's choose 10 MHz.

$$Q_L = \frac{159}{10} = 16$$

 Q_U is generally limited by the inductor. The manufacturer's data generally specifies an unloaded Q at a certain frequency. For a T-30-12 powdered iron core material, Q_U of 120

is typical at this frequency. The external capacitors normally have a higher unloaded Q which can be neglected.

Our first design equations:

$$Q_U = \frac{1}{\omega_o L G_P} = 120$$
$$Q_L = \frac{B}{G_{total}} = \frac{1/\omega_o L}{G_S + G_L + G_P} = 16$$

Assume that $G_S = G_L = 1/1.5k = 6.7 \times 10^{-4}$. That is, design the tapped C network to provide a transformed impedance of 1.5k. This will get close to the solution needed.

So, now we have two equations with 2 unknowns. We can solve for L and for G_{P} .

 $G_P = 2.04 \text{ x } 10^{-4} \text{S}$ or $R_P = 4900 \text{ ohms.}$ $B = Q_U G_P = 2.44 \text{ x } 10^{-2} \text{ S}$

$$L = \frac{1}{\omega_0 B} = 41nH$$

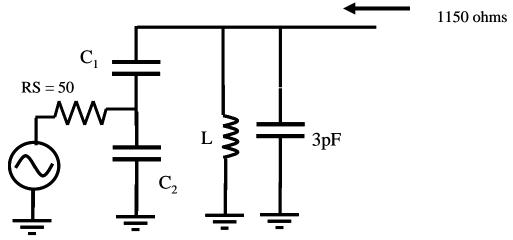
This is a rather small value and will require some care in layout to implement accurately on a PC board.

Check insertion loss:

$$IL = 20\log\left(1 - \frac{Q_L}{Q_U}\right) = -1.2 \, dB$$

If we required less loss, then a wider BW or higher Q_U is necessary.

Next, design the tapped C network to give $1.5k||R_P = 1150$ ohms.



Because L is known, we can calculate the required C_{total} to resonate at 159 MHz.

$$C_{total} = \frac{1}{\omega_o^2 L} = 24 \, pF$$

Now, deduct the 3 pF from C_{total} so that is is absorbed into the resonator. Design C1 and C2 for 21, not 24 pF.

The transformation ratio relates C_1 and C_2 . It should transform 50 ohm source into the load impedance, 1500 ||RP = 1150 ohms.

$$n = \frac{C_1 + C_2}{C_1} = \sqrt{\frac{1150}{50}} = 4.8$$

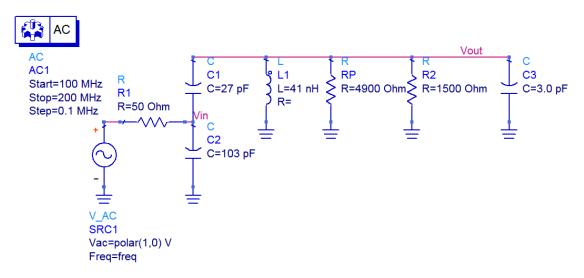
The series combination of C_1 and C_2

$$\frac{C_1 C_2}{C_1 + C_2} = 24 - 3 = 21 \, pF$$

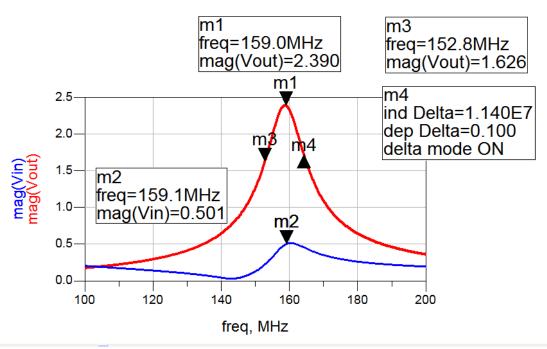
2 equations; 2 unknowns. Solve for C_1 and C_2 .

$$C_1 = 27 \text{ pF}; C_2 = 103 \text{ pF}.$$

Check the result with ADS:



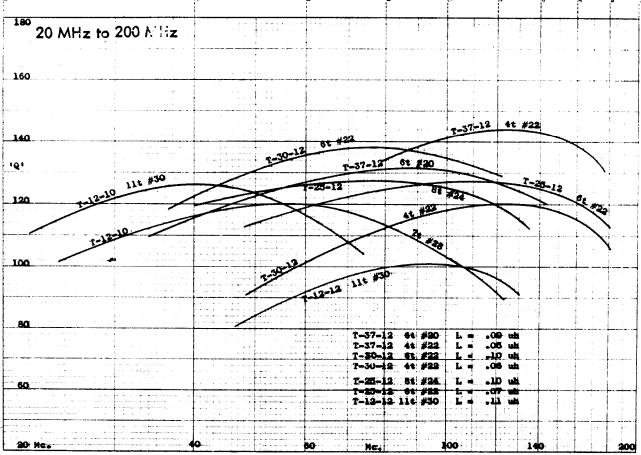
A small signal AC simulation is performed which includes the RP and excess C of the load. If the load is matched correctly to the source, we should see half of the source voltage at node Vin, 0.5V (available power). Vout should be $4.8 \times 0.5 = 2.4$ V.



Everything looks as expected except for the bandwidth, 11.4 MHz rather than 10 MHz. This is due to the effect of R_P . If it mattered, we could go back through again and include this effect and get a 10 MHz BW, but most applications as this one are not so critical. The important thing is to get the correct impedance transformation.

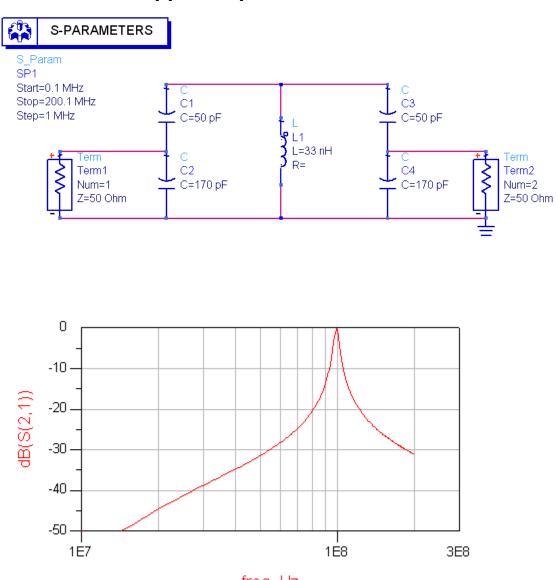
Of course, a L network, PI or T network could also have been used here with somewhat less flexibility in choosing loaded Q.





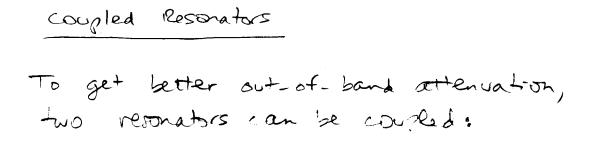
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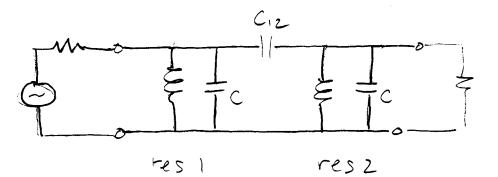
Example of resonator used as bandpass filter: 50 ohms to 50 ohms. 100 MHz. The tapped C transforms up to 1000 ohms for this example. You can work through the design – similar to the earlier example.

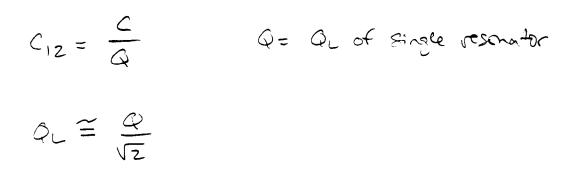


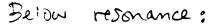
Tapped Capacitor Resonator

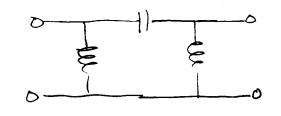
freq, Hz





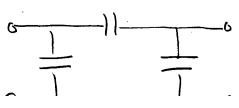


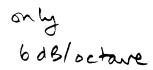




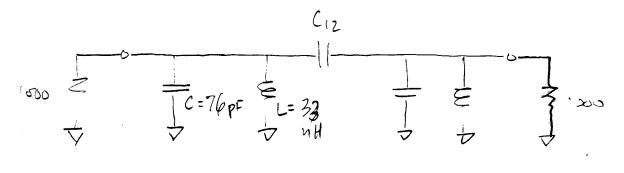
18 dB/ octave rollas





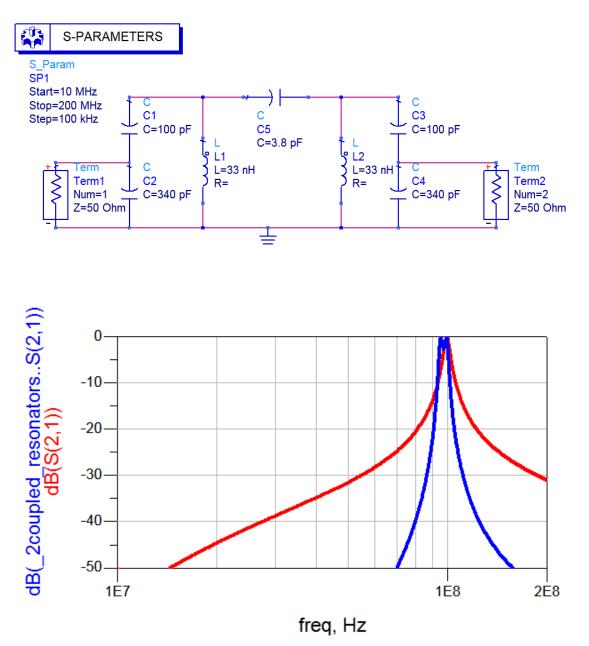


Look at 1st example. use for worded resonator:



$$C_{12} = \frac{76 \, \text{pF}}{20} = 3.8 \, \text{pF}$$

$$QL = \frac{20}{\sqrt{2}} = 14$$



Note the improvement in the stopband rejection provided by the coupled resonator compared with the single resonant tapped C circuit.

Temperature Compensation of Resonant Circuits

Oscillators are frequently used to set the transmit or receive frequency in a communication system. While many applications use a phase locked loop technique to correct for frequency drift, it is good practice to build oscillators with some attempt to minimize such drift by selecting appropriate components.

Inductors and capacitors often drift in value with temperature. Permeability of core materials or thermal expansion of wire causes inductance drift. Variations in dielectric constant with temperature in capacitors is the main source of drift for these components.

Temperature drift is expressed as a temperature coefficient in ppm/°C or %/temp range.

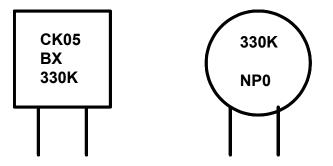
Capacitors

Dielectric type	Temp coefficient (TC)	Temp range
COG (or NP0)	+/- 30 ppm/°C	-55 to +125C
X7R (BX)	- 1667 (+15% to -15%)	-55 to +125
Z5U	-10^4 (+22% to -56%)	10 to 85

The 3 most common types of dielectrics for RF capacitors are:

Clearly, the Z5U is not much good for a tuned circuit and should be used for bypass and AC coupling (DC block) applications where the value is not extremely critical. At lower radio frequencies, polystyrene capacitors can be used. These have a - 150 ppm/C TC.

The C0G and X7R can be used in tuned circuits if their values are selected to compensate for the inductor drift.



The two leaded capacitors above illustrate the labels found on typical capacitors of the X7R and NP0 types. The value is given by the numerals: 330. In this example, this is 33 pF. It goes 1^{st} significant digit (3), 2^{nd} significant digit (3), and multiplier (10^{0}). The letter K is the tolerance, which is +/- 10%.

As always, the parasitic inductance and self resonance of any capacitor must be considered for RF applications.

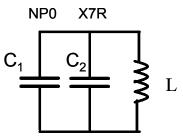
Inductors

There are many types of inductor core materials which are intended for different frequency ranges, permeability, and TC. Powdered Iron and Ferrites are the two categories of these materials.

For example, the material you will have available for the VCO lab is powdered iron, Type 12 (green/white). This is useful from 50 to 200 MHz and gives Qu in the 100 - 150 range. $\mu/\mu_0 = 4$. Manufacturer's data sheets can be found on the web that specify TCs for the many materials. This one has a weird TC vs temperature behavior, but we are mainly interested in the 25 to 50C range for this project.

Temperature range	ТС
25 – 50C	+50 ppm/C
50 - 75	- 50
75 – 125	+ 150

So, how can you compensate for component drift?



The equation below shows how the TCs of individual components combine¹. Suppose that the inductor was resonated with a drift free capacitor (NP0). The frequency drift will be -25 ppm/C. If the design frequency is 100 MHz, this corresponds to a drift of 2.5 kHz/C. But, the equation shows that you can set the total frequency TC (TCF) of a circuit to zero by combining capacitors with different TCs.

$$TCF = \frac{\Delta f}{f_0} = -\frac{1}{2} \left(TC_L + TC_{C1} \frac{C1}{C_{TOTAL}} + TC_{C2} \frac{C2}{C_{TOTAL}} \right)$$

Thus, if the inductor has a positive TC, you can correct for temperature drift with the right combination of non drift and drifty capacitors. In this case, we want the total capacitance of C1 and C2 to have a net TC of -50 ppm/C. The best oscillators will be designed with components with low intrinsic TCs so that you do not have to compensate them with different components having large and possibly unreliable TCs.

¹ W. Hayward, R. Campbell, and B. Larkin, Experimental Methods and RF Design, ARRL Press, 2003.